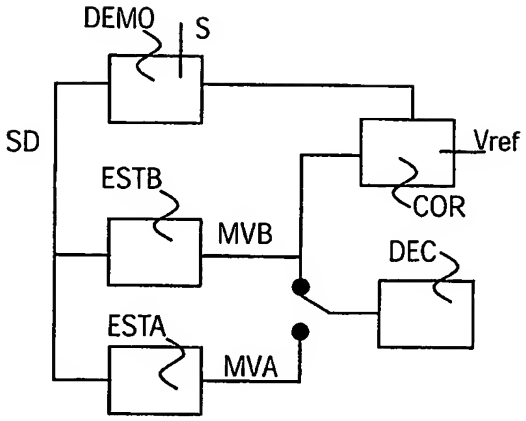


FIG.1

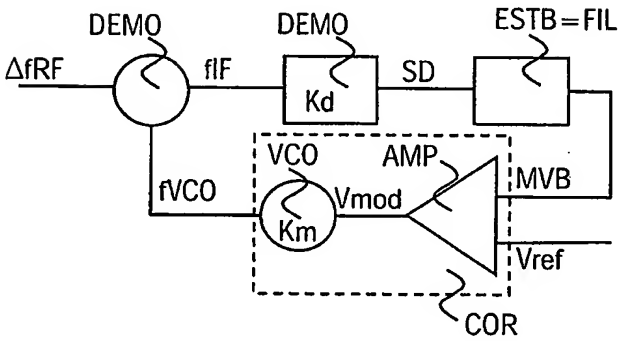


FIG.2

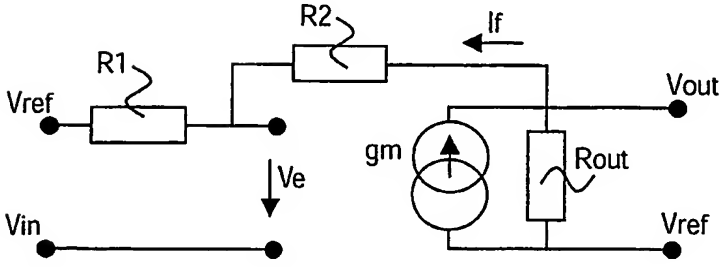
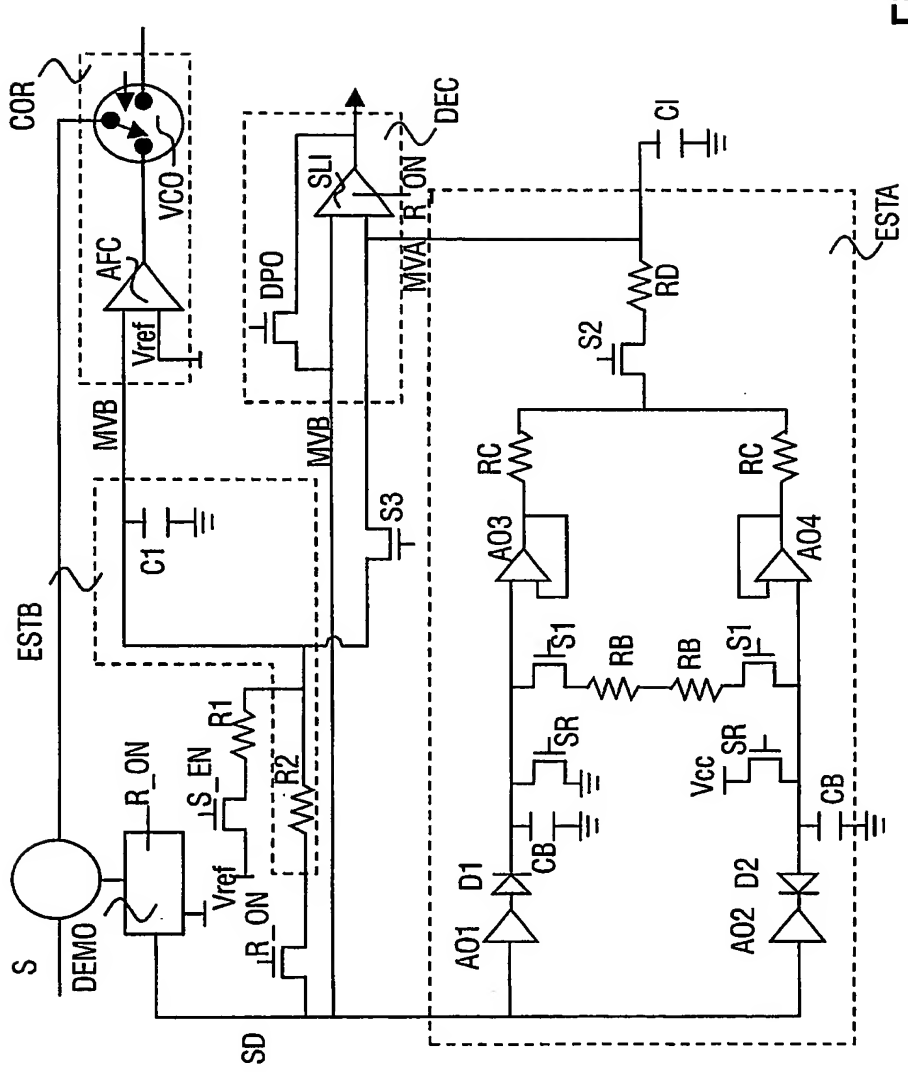


FIG.3

FIG.4


COR
VCO
AFC
Vref
DPO
SLI
DEC
MVA
R_ON
CI
ESTA
RD
S2
MVB
C1
S3
RC
AO3
AO4
S1
RB
SR
Vcc
CB
D1
D2
AO1
AO2
ESTB
R1
R2
S_EN
R_ON
S
DEMO
SD

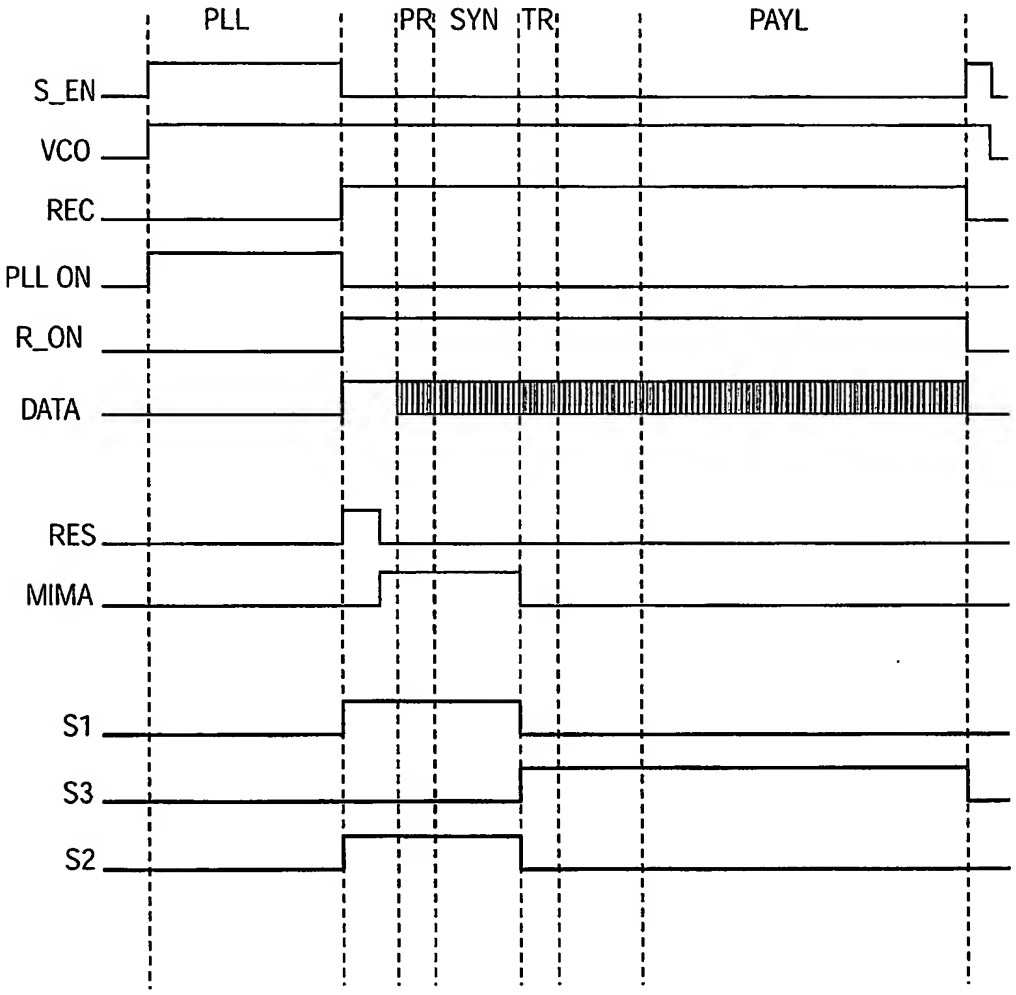


FIG.5

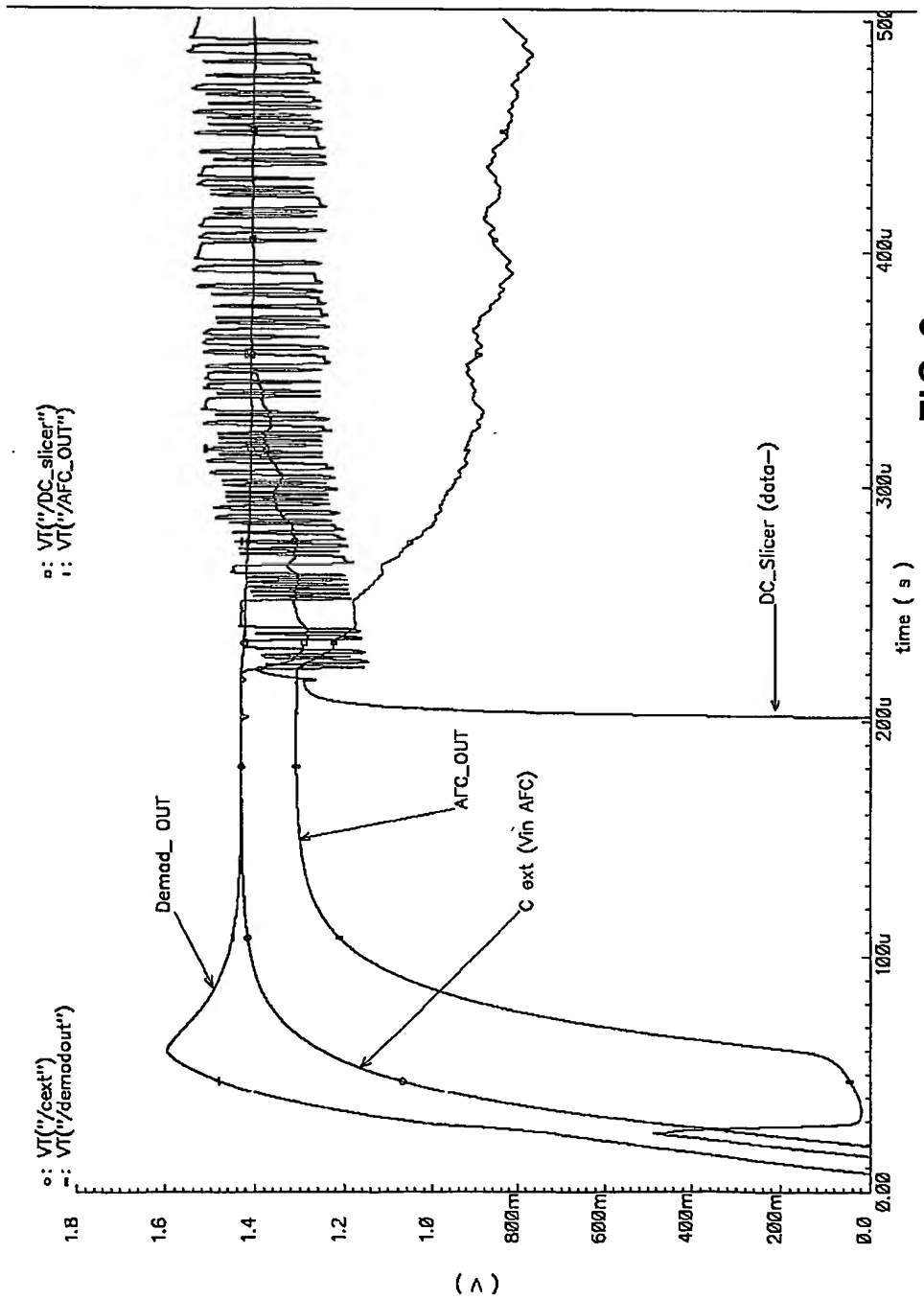

o: VT("/cext")
=: VT("/demadout")
o: VT("/DC_slicer")
=: VT("/AFC_OUT")
Demad_ OUT
AFC_OUT
C ext (Vin AFC)
DC_Slicer (data-)
time (s)
1.8
1.6
1.4
1.2
1.0
800m
600m
400m
200m
0.0
0.00
100u
200u
300u
400u
500
(v)

FIG.6